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PATENT Att'y Dkt.: 2207/12004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Stephan J. JOURDAN

Serial No.: 09/964,807

Filed: September 28, 2001

For: DEFERRAL OF DEPENDENT LOADS

UNTIL AFTER EXECUTION OF

COLLIDING STORES

Assignee: Intel Corporation

Examiner: Shane F. Gerstl

Art Unit: 2183

SUPPLEMENT TO PETITION TO CORRECT INVENTORSHIP PURSUANT TO 37 C.F.R. § 1.48 (a)

MAIL STOP PETITION

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

SIR:

Supplemental to the Petition to Correct Inventorship pursuant to 37 C.F.R. § 1.48(a) that was filed November 23, 2004 in the above-identified application, submitted herewith is an executed declaration and assignment by the inventors to replace the declaration and assignment that were previously filed with the Petition. The enclosed declaration and assignment correct an error in the previously filed documents, in which the application serial number was inadvertently omitted.

The Office is hereby authorized to charge any fees or credit any overpayment to Kenyon & Kenyon's Deposit Account No. 11-0600.

Respectfully submitted,

Date: November 22, 2004

Cassandra T. Swain, Ph.D. Registration No. 48,361

(Attorney for Intel Corporation)

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